

| FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT | | | | ATTY. DOCKET NO. 1875.2570002/RES/GSB | | APPLICATION NO. To Be Assigned | |
|--|-----|--------------------|---|--|-----------------|-----------------------------------|-------------|
| | | | | FIRST NAMED INVENTOR Eric B. BLECKER | | ART UNIT To Be Assigned | |
| | | | | FILING DATE Herewith (April 14, 2004) | | | |
| U.S. PATENT DOCUMENTS | | | | | | | |
| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB-CLASS | FILING DATE |
| PN | AA1 | 4,345,213 | 08/1982 | Shchade, Jr., Otto H. | 330 | 253 | |
| PN | AB1 | 6,563,381 B1 | 05/2003 | Strong, Alexander Mark | 330 | 253 | |
| PN | AC1 | 6,559,720 B1 | 05/2003 | Huijsing <i>et al.</i> | 330 | 253 | |
| PN | AD1 | 6,657,486 B2 | 12/2003 | Kimura, Katsuji | 327 | 563 | |
| PN | AE1 | 5,808,513 | 09/1998 | Archer, Donald M. | 330 | 253 | |
| | AF1 | | | | | | |
| | AG1 | | | | | | |
| | AH1 | | | | | | |
| | AI1 | | | | | | |
| | AJ1 | | | | | | |
| | AK1 | | | | | | |
| FOREIGN PATENT DOCUMENTS | | | | | | | |
| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUB-CLASS | TRANSLATION |
| | AL1 | | | | | | Yes No |
| | AM1 | | | | | | Yes No |
| | AN1 | | | | | | Yes No |
| | AO1 | | | | | | Yes No |
| | AP1 | | | | | | Yes No |
| OTHER (Including Author, Title, Date, Pertinent Pages, etc.) | | | | | | | |
| PN | AR | 1 | Paul R. Gray <i>et al.</i> , "Analysis and Design of Analog Integrated Circuits", 4 th Edition, 2001, pp. 446-450. | | | | |
| | AS | 1 | | | | | |
| | AT | 1 | | | | | |
| EXAMINER | | | | | DATE CONSIDERED | | |
| Patricia Nguyen | | | | | 9/3/04 | | |
| EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant. | | | | | | | |